

KCO-970 Series

Ceramic SMD Crystal PECL Oscillators

Frequency range	100.000 ~ 400.000 MHz	
Frequency stability	± 50ppm	
Operating temperature range	0 ~ +70	
Storage temperature range	-55 ~ +125	
Input voltage	2.5, 3.0, 3.3V ± 5%	
Input current	88mA Max	
Output Signal	VOH	Vdd-1.02V (Vdd-1.08V : -40~0)
	VOL	Vdd-1.63V (Vdd-1.55V : -40~0)
Output Load	50 (Vdd-2.0)	
Rise & Fall time	1 ns Max	
Duty Cycle	40:60 or 45/55%(option)	
Jitter (Cycle to Cycle Jitter RMS, 1 sigma)	5ps Max.	
Jitter (Phase jitter RMS, 12KHz to 20MHz Band)	1ps Max.	

All specifications are subject to change without notice. Option: *Tighter Stability, 45/55 Duty cycle

Top view dimensions: 7.0 ± 0.2 (width), 5.0 ± 0.2 (height), 1.8 ± 0.2 (thickness). Pin locations #1-#6 are marked.

Side view shows a height of 1.8 ± 0.2 mm.

TEST CIRCUIT diagram shows a square package with pins #1-#6. Dimensions: 5.08 (width), 2.6 (height), 1.4 (pin width), 1.0 (pin height).

Enable-disable	
input pin # 1	output pin # 4, # 5
H or open	RF-signal
0.7V MIN	
L	HI-Z
0.3V MAX	

OE Oscillation time : 100ns max

LAND PATTERN LAYOUT (EXAMPLE)

OUTPUT WAVEFORM

1 E/D
 # 2 N/C
 # 3 Vss
 # 4 OUT
 # 5 COUT
 # 6 VDD

Dimension in mm