

VCXO-300 Series

PECL SMD Voltage Controlled Crystal Oscillators

Frequency range	155.520 MHz/156.250 MHz				
Frequency stability	± 50ppm				
Operating temperature range	0 ~ +70				
Input voltage	3.3V ± 5%				
Frequency Deviation	± 100ppm min. (Absolute pull range ± 50ppm min)				
Control Voltage	1.65 ± 1.5V				
Input current	100mA Max				
Output Signal	VOH	Vdd-1.02V			
	VOL	Vdd-1.63V			
Output Load	50 (Vdd-2.0V)				
Rise & Fall time	1 ns Max				
Duty Cycle	40:60				
Jitter (Cycle to Cycle jitter RMS, 1 sigma)	5ps Max.				
Jitter (Phase jitter RMS, 12KHz to 20MHz Band)	1ps Max.				
Phase Noise (Typical) (dBc/Hz)	10Hz	100Hz	1kHz	10kHz	100kHz
	-72	-107	-130	-145	-145

All specifications are subject to change without notice.

Top view dimensions: 7.0 ± 0.2 mm (width), 5.0 ± 0.2 mm (height).
 Side view dimensions: 1.8 ± 0.2 mm (height).
 Test circuit view dimensions: 5.08 mm (width), 2.6 mm (height), 1.4 mm (width of pads).

Enable-disable	
input pin # 1	output pin # 4, # 5
H or open	RF-signal
0.7V MIN	
L	HI-Z
0.3V MAX	

OE Oscillation time: 100ns max

LAND PATTERN LAYOUT (EXAMPLE)

TEST CIRCUIT

- #1 Vccn
- #2 N/C
- #3 Vss
- #4 OUT
- #5 COUT
- #6 VDD

OUTPUT WAVEFORM.

Dimension in mm